In The Drawings:

Please substitute the attached clean drawing of Fig. 1 for the pending drawing of Fig. 1. The amended portion is the addition of the several coprocessors and related bus connection.

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PAGE 4/12 * RCVD AT 12/6/2004 7:20:42 PM [Eastern Standard Time] * SVR:USPTO-EFXRF-1/0 * DNIS:8729306 * CSID:19496600809 * DURATION (mm-ss):03-08

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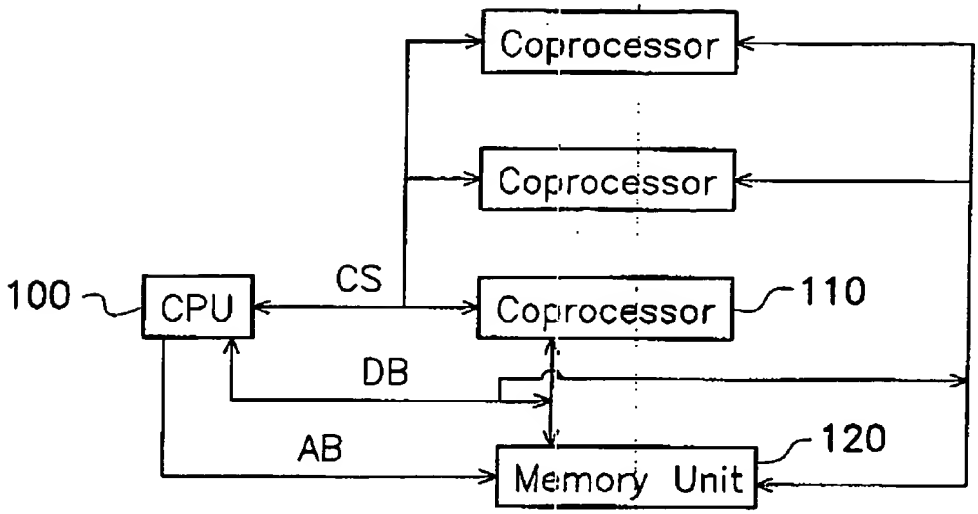

FIG. 1

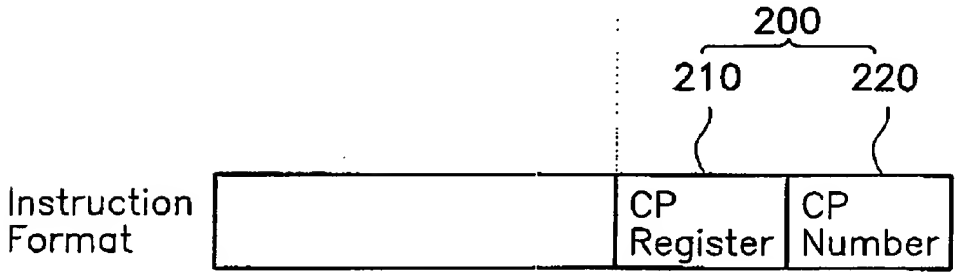

FIG. 2

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